

VIGNESH SARAVANAPERUMAL
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Education:

Master of Science in Computer Engineering (Computer Networks and Security) May 2011
University of Central Florida Orlando, Florida
GPA: 3.5/4.0

Bachelor of Technology in Electronics and Communication Engineering 2004-2008
SRM University Chennai, India
First Class with Distinction

Professional Experience:

Research and Development Engineer (RF and VLSI Domain) June 2008-June 2009
VI Microsystems Chennai, India

- Designed and Developed IQ Modulation based RF Transmitter and Receiver at 2.4GHZ.
- Developed significant number of Sample VHDL codes for VLSI trainer kits used in Educational Institutions (Xilinx Spartan 3E FPGA-XC3S400 - 4PQ208, XC3S500E – FT256).

Undergraduate Experience:

Undergraduate Final year project SRM University - Diabetic foot care India
➤ Designed and developed an Ambulatory Blood Pressure Monitor using ARM 7 Processor - complete solution including hardware and software using C++

Relevant coursework in Master Program:

- a) Computer Network Design b) Network Optimization c) Advanced Computer Networks
- d) Wireless Networks e) Wireless Security f) Theory and Analysis of Algorithms
- g) Complex Networks h) Random Processes

Relevant projects in Master Program:

- Designing a Load balancing based Multipath Routing Algorithm for MANET.
- Designing a Scheduling based MAC Protocol Algorithm for Underwater Wireless Sensor Networks.
- Content Delivery Networks: Survey of Attacks, Potential Threats and Defense Mechanisms.
- Malware Distribution Technique in Internet through iFRAMES.
- Implementation of static buffer overflow attack in Linux Environment.
- Survey of Graph Partitioning Methods for Load balancing.
- Forensic Reference to Windows Registry.
- Survey of Security in Bluetooth Context.

Technical Skills:

- Languages: C, C++, JAVA, Intel Assembly, Matlab Programming and VHDL.
- Software used: Wireshark, Opnet, Pajek, Dreamweaver, and ModelSim.
- Web Development languages: HTML, CSS.
- Operating System: Windows and Linux.